WELLS ST. JCHN, P.S.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	10/695,727
Filing Date	October 28, 2003
Inventor	
Assignee	
Group Art Unit	
Examiner	
Attorney Docket No	Ml22-2433
Title: Semiconductor Substrate Processing	Chamber and Accessory Attachment
Interfacial Structure	RECEIVED

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

FEB 1 2 2004

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- 3. Supplemental IDS with Form PTO-1449

4. Cited Reference (1)

Dated: __

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ENCLOSURES (Check all that apply)						
Fee Transmittal Form Fee Attached Amendment/Reply After Final Affidavits/declar Extension of Time Requ Express Abandonment Information Disclosure S Certified Copy of Priority Document(s) Response to Missing Pa Incomplete Application Response to Missing Pa Incomplete Application	ration(s) PP PP Request Request Remark Statement y arts/ ssing Parts	rawing(s) icensing-related Papers letition letition to Convert to a rovisional Application ower of Attorney, Revocation hange of Correspondence Addre erminal Disclaimer lequest for Refund	After Allowance communication to Technology Center (TC) Appeal Communication to Board of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Proprietary Information			
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gettering, preparing, and submitting the completed application form to this USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.						

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WELLS ST. JOHN, P.S.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent A	Application Serial No	10/695,727
		October 28, 2003
		Ross S. Dando et al.
		Micron Technology, Inc.
		2812
Attorney	v Docket No	MI22-2433
		021567
	Semiconductor Substrate Pro	
· ·	Attachment Interfacial Structu	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 2-12-64

Reg. No. 32,268

Sheet 1 of 5

Form PTO-	1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2433			SERIAL NO. 10/695,727				
	LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT: Craig M. Carpenter et al,							
						FILING DATE: October 28, 20			GR0 2812	OUP ART UNIT : 2	
U.S. PATENT DOCUMENTS											
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